

01/27/99



JCS95 U.S. PTO

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEJCS95 U.S. PTO  
09/238262

01/27/99

UTILITY PATENT APPLICATION  
TRANSMITTAL LETTER  
UNDER 37 C.F.R. 1.53(b)

ATTORNEY DOCKET NO.:

10191/955

## Address to:

Assistant Commissioner for Patents  
Washington D.C. 20231  
Box Patent Application

Transmitted herewith for filing is a patent application.

Inventor(s): Joerg SCHAEFER, Peter LINKE, Albrecht SCHWILLE and  
Helmut BAUMANNFor: **METHOD OF PRODUCING STRUCTURED WAFERS**

## 1. Enclosed are:

- ☒ 2 sheet(s) of drawing(s).  
☐ An Assignment of the invention and an accompanying Assignment Recordal.  
☒ A certified copy of German Patent Application No. 1 98 03 186.6-33 filed on  
January 28, 1998, on which the claim to priority is based.  
☒ A declaration/power of attorney (unsigned).  
☐ An Information Disclosure Statement along with an accompanying  
PTO-1449 form.  
☐ Other: \_\_\_\_\_

## 2. The filing fee has been calculated as shown below:

	NUMBER FILED	NUMBER EXTRA*	RATE (\$)	FEE (\$)
BASIC FEE				760.00
TOTAL CLAIMS	9 - 20 =	0	18.00	0.00
INDEPENDENT CLAIMS	1 - 3 =	0	78.00	0.00
MULTIPLE DEPENDENT CLAIM PRESENT			260.00	0
*Number extra must be zero or larger			TOTAL	760.00
If the applicant is a small entity under 37 C.F.R. §§ 1.9 and 1.27, then divide total fee by 2, and enter amount here.			SMALL ENTITY TOTAL	

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3. Please charge the required application filing fee of **\$760.00** to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**.
4. The Commissioner is hereby authorized to charge payment of the following fees, associated with this communication or arising during the pendency of this application, or to credit any overpayment, to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**:
- A. Any additional filing fees required under 37 C.F.R. § 1.16;
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  - E. Any additional miscellaneous fees under 37 C.F.R. § 1.21.
5. A duplicate copy of this letter is enclosed for that purpose.

Respectfully submitted,

Dated: 1/27/98

By: Richard L. Mayer  
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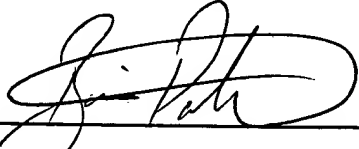
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Method of Producing Structured Wafers

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